

I hereby certify that this correspondence is being
faxed only to **Examiner O. Nadav at 1 703 308-7722 or 308-7724** to:
Assistant Commissioner for Patents,
Washington, D.C. 20231, on November 20, 2001

TOWNSEND and TOWNSEND and CREW LLP

By

L. Olivier
Lata Olivier

PATENT

Attorney Docket No. 019717-001510

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of:

G. Gutierrez

Application No.: 09/650,275

Filed: August 29, 2000

For: **SEAL RING FOR INTEGRATED
CIRCUITS**

Examiner: O. Nadav

Art Unit: 2811

RESPONSE TO RESTRICTION
REQUIREMENT

Assistant Commissioner for Patents
Washington, D.C. 20231

Sir:

REMARKS

The Examiner has issued a restriction requirement. The Examiner states that claims 1-8 and 14-27 are drawn to a semiconductor device.

Applicant elects, without traverse, Group I. Applicant respectfully submits that claims 1-8 and 14-27 are within Group I.

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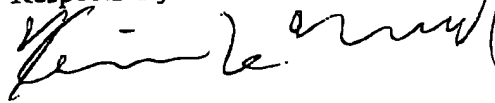
*St. Green
11/26/01*

PATENT

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If the Examiner feels for any reason that a personal communication would expedite the prosecution of this application, the Examiner is invited to contact the undersigned at the telephone number below.

Respectfully submitted,



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